



# COMPETE INDIA ZONE

## AN ENGINEERING ENTERPRISE BY IITians

### Electronics and Communications Engineering (ECE) – Subjectwise GATE Analysis

Year	Network Theory	Electronic Devices	Analog Circuits	Digital Circuits	Signals & Systems	Control Systems	Communications	EMFT	Microprocessor	Engg. Maths	Reasoning & Aptitude	Total
2009	10	8	12	14	15	10	11	8	0	12		100
2010	9	10	11	6	7	9	11	8	2	12	15	100
2011	9	9	10	7	11	9	10	10	2	8	15	100
2012	12	7	8	6	8	9	10	14	0	11	15	100
2013	15	3	14	5	10	9	12	6	1	10	15	100
2014 (Set A)	10	4	15	8	9	8	10	8	0	13	15	100
2014 (Set B)	10	8	10	9	9	8	10	8	0	13	15	100
2014 (Set C)	10	8	10	8	9	8	11	8	0	13	15	100
2014 (Set D)	10	9	10	8	9	8	10	8	0	13	15	100

2015 (Set A)	10	8	13	9	10	9	10	10	0	11	10	100
2015 (Set B)	9	8	13	9	10	9	10	10	0	12	10	100
2015 (Set C)	10	8	13	9	6	12	10	10	0	12	10	100
2016 (Set A)	3	11	7	6	10	11	12	10	2	13	15	100
2016 (Set B)	6	11	11	7	7	11	10	7	2	13	15	100
2016 (Set C)	6	10	10	7	11	10	9	7	2	13	15	100
2017 (Set A)	5	11	9	8	8	10	10	7	3	14	15	100
2017 (Set B)	6	13	9	9	8	9	11	8	0	12	15	100
2018	6	13	7	9	8	9	11	8	0	14	15	100
2019	5	15	10	5	10	10	12	9	0	9	15	100
Average	8.5	9.2	10.6	7.8	8.5	9.4	10.5	8.6	0.7	12.0	14.2	100.0

IES | GATE | SDE